

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: K. OSADA, et al.

Application No.: Rule 1.53(b) Continuation of U.S. Patent Application Serial No. 10/320,516, filed December 17, 2002

Filed: On even date herewith

For: SEMICONDUCTOR MEMORY APPARATUS,  
SEMICONDUCTOR APPARATUS, DATA PROCESSING  
APPARATUS AND COMPUTER SYSTEM

Art Group of Parent: 2818

Examiner of Parent: H. V. Ho

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

October 17, 2003

Sir:

Prior to examination, please amend the above-identified application as listed below and as set forth on the following pages:

**Amendments to the Specification and Title and**

**Remarks follow the amendments.**